

IN THE SPECIFICATION

Please amend Page 3, Lines 3-9, as follows:

31/ In System-On-a-Chip implementations like the Geode™ SC1400 described above, different modules on the chip may be analog and/or digital, according to their designed function. For example, the bridge and the processors are digital modules, while the super input/output (I/O) module is an analog module. The super input/output (I/O) module comprises analog elements because it processes analog signals.

Please amend Page 21, Line 24 to Page 22, Line13, as follows:

22/ FIGURE 3 illustrates an exemplary embodiment of a power sense cell 300 for use with the apparatus and method of the present invention. Power sense cells 211, 221, 224, and 231 may each comprise a power sense cell of the type illustrated by power sense cell 300. Power sense cell technology is well known. Various implementations of power sense cells may be adequate for use in the present invention. Power sense cell 300 comprises a Schmitt trigger circuit 304. Schmitt trigger circuit 304 is a bistable circuit in which a transition from the output state from a High level to a Low level occurs at a lower output voltage than the input voltage required for a transition of the output state from a Low level to a High level. This feature is indicated by the hysteresis loop shown on the Schmitt trigger symbol in FIGURE 3.